

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	3677	(ic (integrat\$3 near3 circuit\$1) processor\$1 cpu microprocessor\$1 controller\$1) with (each different\$3 separat\$3 another\$1 other\$1 many) with ((secret\$2 near3 information\$1) (secret\$2 near3 data\$1) key\$1 password\$1 encrypt\$4 cipher\$9 crypto\$9)	USPAT; IBM_TDB	OR	OFF	2008/10/10 12:27
L2	331313	(location\$1 area\$1 address\$3 module\$1 pin\$1 slot\$1) near6 (memor\$3 storage\$1 rom ram eeprom eeprom)	USPAT; IBM_TDB	OR	OFF	2008/10/10 12:27
L3	65612	(ic (integrat\$3 near3 circuit\$1)) with (processor\$1 cpu microprocessor\$1 controller\$1)	USPAT; IBM_TDB	OR	OFF	2008/10/10 12:27
L4	11999	L2 with ((secret\$2 near3 information\$1) (secret\$2 near3 data\$1) key\$1 password\$1 encrypt\$4 cipher\$9 crypto\$9)	USPAT; IBM_TDB	OR	OFF	2008/10/10 12:27
L5	1454	L4 and L3	USPAT; IBM_TDB	OR	OFF	2008/10/10 12:27
L6	358	L5 and (process\$2 near4 (same\$1 identical\$2 mirror\$1))	USPAT; IBM_TDB	OR	OFF	2008/10/10 12:27
L7	63	L6 and L1	USPAT; IBM_TDB	OR	OFF	2008/10/10 12:27

L8	38658	(ic chip\$1 (integrat\$3 near3 circuit\$1)) near6 ((secret\$2 near3 information\$1) (secret\$2 near3 data \$1) key\$1 password \$1 encrypt\$4 cipher \$9 crypto\$9 pin code \$1)	USPAT; IBM_TDB	OR	OFF	2008/10/10 12:27
L9	59053	L3 and process\$2	USPAT; IBM_TDB	OR	OFF	2008/10/10 12:27
L10	13262	((713/152) or (713/189) or (713/190) or (713/192) or (713/193) or (713/375) or (713/400) or (726/34) or (711/1) or (711/5) or (711/100) or (711/101-105) or (711/111) or (711/147) or (711/148) or (711/150) or (711/152) or (71/209) or (380/201) or (380/228) or (380/240) or (380/255)).CCLS.	USPAT; IBM_TDB	OR	OFF	2008/10/10 12:27
L11	1827	L10 and L9	USPAT; IBM_TDB	OR	OFF	2008/10/10 12:27
L12	484	L11 and L8	USPAT; IBM_TDB	OR	OFF	2008/10/10 12:27
L13	21746	(ic chip\$1 (integrat\$3 near3 circuit\$1)) near8 (location\$1 area\$1 address\$3 module\$1 pin\$1 slot \$1) near4 (memor\$3 storage\$1 rom ram eeprom eeprom)	USPAT; IBM_TDB	OR	OFF	2008/10/10 12:27

L14	30	L13 with ((secret\$2 near3 information\$1) (secret\$2 near3 data \$1) key\$1 password \$1 encrypt\$4 cipher \$9 crypto\$9) with (each different\$3 separat\$3 another\$1 other\$1 many)	USPAT; IBM_TDB	OR	OFF	2008/10/10 12:27
L15	7	L12 and L14	USPAT; IBM_TDB	OR	OFF	2008/10/10 12:27
L16	143	L13 with ((secret\$2 near3 information\$1) (secret\$2 near3 data \$1) key\$1 password \$1 encrypt\$4 cipher \$9 crypto\$9 pin code \$1) with (each different\$3 separat\$3 another\$1 other\$1 many)	USPAT; IBM_TDB	OR	OFF	2008/10/10 12:27
L17	43	L9 and L16	USPAT; IBM_TDB	OR	OFF	2008/10/10 12:27
L18	43	(ic (integrat\$3 near3 circuit\$1)) and L17	USPAT; IBM_TDB	OR	OFF	2008/10/10 12:27
L19	21	L18 and ((secret\$2 near3 information\$1) (secret\$2 near3 data \$1) key\$1 password \$1 encrypt\$4 cipher \$9 crypto\$9)	USPAT; IBM_TDB	OR	OFF	2008/10/10 12:27
L20	1388	L4 and L9	USPAT; IBM_TDB	OR	OFF	2008/10/10 12:27
L21	50193	L2 with ((secret\$2 near3 information\$1) (secret\$2 near3 data \$1) key\$1 password \$1 encrypt\$4 cipher \$9 crypto\$9 pin code \$1)	USPAT; IBM_TDB	OR	OFF	2008/10/10 12:27
L22	5485	L9 and L13	USPAT; IBM_TDB	OR	OFF	2008/10/10 12:27
L23	2058	L8 and L22	USPAT; IBM_TDB	OR	OFF	2008/10/10 12:27
L24	199	L12 and L13	USPAT; IBM_TDB	OR	OFF	2008/10/10 12:27

L25	25	L24 and ((tamper\$3 obscure\$4 fault\$1 unsafe malicious\$2 insecure \$4 secure\$6 unsecure \$6) near5 (detect\$4 monitor\$3 scan\$4))	USPAT; IBM_TDB	OR	OFF	2008/10/10 12:27
L26	3524	(tamper\$3 obscure\$4 fault\$1 fraud\$7 unsafe malicious\$2 insecure\$4 secure\$6 unsecure\$6) near5 (detect\$4 monitor\$3 scan\$4) near8 (memory\$4 rom ram eprom eeprom storage\$1)	USPAT; IBM_TDB	OR	OFF	2008/10/10 12:27
L27	12	L24 and L26	USPAT; IBM_TDB	OR	OFF	2008/10/10 12:27
L28	354	(ic chip\$1 (integrated\$3 near3 circuit\$1)) with (tamper detect\$4 obscure\$9 intrusion\$4 intrusion\$4) near4 (memory\$3 storage\$1 rom ram eeprom eprom nonvolatile\$1 non-volatile\$1) with (process\$3 microprocess\$3 micro-processor\$1 cpu)	USPAT; IBM_TDB	OR	OFF	2008/10/10 12:27
L29	181361	(process\$2 execute\$4 run\$4 code\$1 instruction \$4) near7 (same\$1 identical\$2 mirror\$1) near6 (software\$1 program\$3 process \$2)	USPAT; IBM_TDB	OR	OFF	2008/10/10 12:27
L30	138321	(process\$2 execute\$4 run\$4 code\$1 instruction \$4) near4 (same\$1 identical\$2 mirror\$1) near4 (software\$1 program\$3 process \$2)	USPAT; IBM_TDB	OR	OFF	2008/10/10 12:27
L31	96	L28 and L30	USPAT; IBM_TDB	OR	OFF	2008/10/10 12:27

L32	12707	L2 with ((secret\$2 near3 information\$1) (secret\$2 near2 data \$1) (secur\$7 near2 data\$1) (secur\$7 near2 information\$1) (secret\$2 near2 content\$1) (secur\$7 near2 content\$1) key \$1 password\$1 encrypt\$4 cipher\$9 crypto\$9 (secret\$2 near2 (pin code\$1)))	USPAT; IBM_TDB	OR	OFF	2008/10/10 12:27
L33	447	L13 with ((secret\$2 near3 information\$1) (secret\$2 near2 data \$1) (secur\$7 near2 data\$1) (secur\$7 near2 information\$1) (secret\$2 near2 content\$1) (secur\$7 near2 content\$1) key \$1 password\$1 encrypt\$4 cipher\$9 crypto\$9 (secret\$2 near2 (pin code\$1)))	USPAT; IBM_TDB	OR	OFF	2008/10/10 12:27
L34	2	L31 and L33	USPAT; IBM_TDB	OR	OFF	2008/10/10 12:27
L35	97	(ic chip\$1 (integrat\$3 near3 circuit\$1)) with (tamper detect\$4 obscur\$9 intrusi\$4 intrud\$4) near4 (memor\$3 storage\$1 rom ram eeprom eeprom nonvolatile\$1 non-volatile\$1) with ((secret\$2 near3 information\$1) (secret \$2 near2 data\$1) (secur\$7 near2 data \$1) (secur\$7 near2 information\$1) (secret \$2 near2 content\$1) (secur\$7 near2 content\$1) key\$1 password\$1 encrypt \$4 cipher\$9 crypto\$9 (secret\$2 near2 (pin code\$1)))	USPAT; IBM_TDB	OR	OFF	2008/10/10 12:27
L36	55	L31 and L35	USPAT; IBM_TDB	OR	OFF	2008/10/10 12:27

L37	3994	(tamper\$3 obscur\$4 fault\$1 fraud\$7 unsafe malicious\$2 insecur\$4 secur\$6 unsecur\$6) near8 (detect\$4 monitor\$3 scan\$4) near8 (memor\$4 rom ram eeprom eeprom storage\$1)	USPAT; IBM_TDB	OR	OFF	2008/10/10 12:27
L38	55	L36 and L37	USPAT; IBM_TDB	OR	OFF	2008/10/10 12:27
L39	55	L38 and tamper\$4	USPAT; IBM_TDB	OR	OFF	2008/10/10 12:27
L40	291	simon near3 walmsley	USPAT; IBM_TDB	OR	OFF	2008/10/10 12:27
L41	85	L40 and L3	USPAT; IBM_TDB	OR	OFF	2008/10/10 12:27
L42	11	L41 and L37	USPAT; IBM_TDB	OR	OFF	2008/10/10 12:27
L43	2	(integrate\$1 and circuit\$1 and tamper and line and memory and processor\$1 and chip\$1).dm.	US-PGPUB	OR	ON	2008/10/10 12:27
L44	1	(integrate\$1 and circuit\$1 and location \$1 and non-volatile and plurality and secret\$1 and memory and processor\$1 and chip\$1).dm.	US-PGPUB	OR	ON	2008/10/10 12:27
L45	169	tamper\$3 near5 detect\$4 near3 line\$1	US-PGPUB	OR	ON	2008/10/10 12:27
L46	153	L45 and (ic (integrate \$1 near3 circuit\$1))	US-PGPUB	OR	ON	2008/10/10 12:27
L47	141	(tamper\$3 near5 detect\$4 near3 line \$1) near8 (memory \$1 nonvolatile\$1 non-volatile\$1 rom ram eeprom eeprom)	US-PGPUB	OR	ON	2008/10/10 12:27
L48	396	(tamper\$3 near5 detect\$4) near8 (memory\$1 nonvolatile\$1 non-volatile\$1 rom ram eeprom eeprom)	US-PGPUB	OR	ON	2008/10/10 12:27

L49	248	L48 and (ic (integrate \$1 near3 circuit\$1))	US-PGPUB	OR	ON	2008/10/10 12:27
L50	107	L49 not L47	US-PGPUB	OR	ON	2008/10/10 12:27
L51	179	tamper\$3 near5 detect\$4 near3 line\$1	USPAT; USOCR; EPO; IBM_TDB	OR	ON	2008/10/10 12:27
L52	110	(tamper\$3 near5 detect\$4 near3 line \$1) near8 (memory \$1 nonvolatile\$1 non-volatile\$1 rom ram eeprom eeprom)	USPAT; USOCR; EPO; IBM_TDB	OR	ON	2008/10/10 12:27
L53	305	(tamper\$3 near5 detect\$4) near8 (memory\$1 nonvolatile\$1 non-volatile\$1 rom ram eeprom eeprom)	USPAT; EPO; IBM_TDB	OR	ON	2008/10/10 12:27
L54	301	L53 and (ic (integrate \$1 near3 circuit\$1) cpu microprocessor \$1 processor\$1 chip \$1 circuit\$1)	USPAT; EPO; IBM_TDB	OR	ON	2008/10/10 12:27
L55	110	L52 and (ic (integrate \$1 near3 circuit\$1) cpu microprocessor \$1 processor\$1 chip \$1 circuit\$1)	USPAT; EPO; IBM_TDB	OR	ON	2008/10/10 12:27
L56	301	L54 not L47	USPAT; EPO; IBM_TDB	OR	ON	2008/10/10 12:27
L57	132	L56 and (obscure\$4 obfuscate\$4)	USPAT; EPO; IBM_TDB	OR	ON	2008/10/10 12:27
L58	349	(obscure\$4 obfuscate \$4) near8 (memory \$1 nonvolatile\$1 non-volatile\$1 rom ram eeprom eeprom)	USPAT; EPO; IBM_TDB	OR	ON	2008/10/10 12:27
L59	2	L56 and L58	USPAT; EPO; IBM_TDB	OR	ON	2008/10/10 12:27
L60	0	((tamper\$3 intruder\$4 intruder\$3) near5 detect \$4 near3 (circuit\$3line \$1)) near8 (memory \$1 nonvolatile\$1 non-volatile\$1 rom ram eeprom eeprom)	USPAT; USOCR; EPO; IBM_TDB	OR	ON	2008/10/10 12:27

L61	165	((tamper\$3 intrus\$4 intrud\$3) near5 detect \$4 near3 (circuit\$3 line\$1)) near8 (memory\$1 nonvolatile\$1 non-volatile\$1 rom ram eeprom eeprom)	USPAT; USOCR; EPO; IBM_TDB	OR	ON	2008/10/10 12:27
L62	164	L61 not L47	USPAT; EPO; IBM_TDB	OR	ON	2008/10/10 12:27
L63	4418	(obscure\$4 obfuscate \$4) near5 (operation \$1 attempt\$4 access \$3)	USPAT; EPO; IBM_TDB	OR	ON	2008/10/10 12:27
L64	5176	(obscure\$4 obfuscate \$4) near8 (operation \$1 attempt\$4 access \$3)	USPAT; EPO; IBM_TDB	OR	ON	2008/10/10 12:27
L65	1	L62 and L64	USPAT; EPO; IBM_TDB	OR	ON	2008/10/10 12:27
L66	57644	(decrypt\$9 decipher \$9 decode\$3 obfuscate \$3 obscure\$9) near6 (operation\$1 attempt \$4 access\$3)	USPAT; EPO; IBM_TDB	OR	ON	2008/10/10 12:27
L67	106	L62 and L66	USPAT; EPO; IBM_TDB	OR	ON	2008/10/10 12:27
L68	95	L67 and (ic (integrate \$1 near3 circuit\$1))	USPAT; EPO; IBM_TDB	OR	ON	2008/10/10 12:27

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